IMPORTANT NOTICE

10 December 2015

1. Global joint venture starts operations as WeEn Semiconductors

Dear customer,

As from November 9th, 2015 NXP Semiconductors N.V. and Beijing JianGuang Asset Management Co. Ltd established Bipolar Power joint venture (JV), **WeEn Semiconductors**, which will be used in future Bipolar Power documents together with new contact details.

In this document where the previous NXP references remain, please use the new links as shown below.

WWW - For www.nxp.com use www.ween-semi.com
Email - For salesaddresses@nxp.com use salesaddresses@ween-semi.com

For the copyright notice at the bottom of each page (or elsewhere in the document, depending on the version) "© **NXP Semiconductors N.V. *{year}*. All rights reserved**" becomes "© **WeEn Semiconductors Co., Ltd. *{year}*. All rights reserved**"

If you have any questions related to this document, please contact our nearest sales office via e-mail or phone (details via salesaddresses@ween-semi.com).

Thank you for your cooperation and understanding,

WeEn Semiconductors

BYC10DX-600

Hyperfast power diode

Rev. 1 — 30 June 2011 **Product data sheet**

1. Product profile

1.1 General description

Hyperfast power diode in a SOD113 (2-lead TO-220F) plastic package.

1.2 Features and benefits

- Isolated plastic package
- Low reverse recovery current
- Low thermal resistance
- Reduces switching losses in associated MOSFET

1.3 Applications

- Continuous Current Mode (CCM) Power Factor Correction (PFC)
- Half-bridge/full-bridge switched-mode power supplies
- Half-bridge lighting ballasts

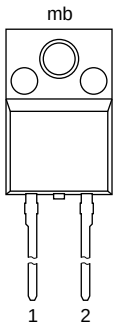
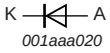
1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{RRM}	repetitive peak reverse voltage		-	-	600	V
$I_{F(AV)}$	average forward current	square-wave pulse; $\delta = 0.5$; $T_h = 41$ °C; see Figure 1; see Figure 2	-	-	10	A
Static characteristics						
V_F	forward voltage	$I_F = 10$ A; $T_j = 25$ °C; see Figure 5	-	2	2.5	V
		$I_F = 10$ A; $T_j = 150$ °C; see Figure 5	-	1.4	1.8	V
Dynamic characteristics						
t_{rr}	reverse recovery time	$I_F = 10$ A; $V_R = 400$ V; $dI_F/dt = 500$ A/µs; $T_j = 25$ °C; see Figure 6	-	18	-	ns

2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode	 SOD113 (TO-220F)	 001aaa020
2	A	anode		
mb	n.c.	mounting base; isolated		

3. Ordering information

Table 3. Ordering information

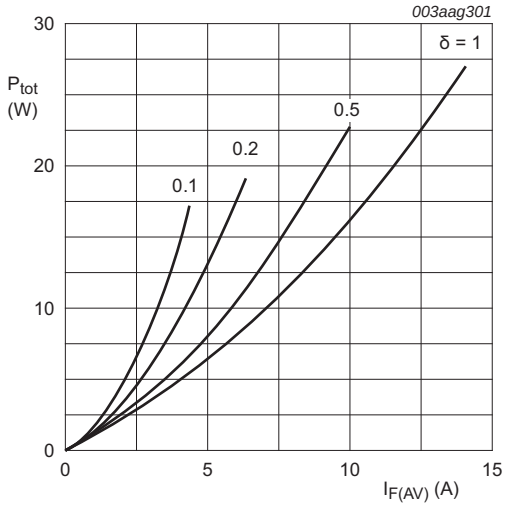
Type number	Package		
	Name	Description	Version
BYC10DX-600	TO-220F	plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 2-lead TO-220 "full pack"	SOD113

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

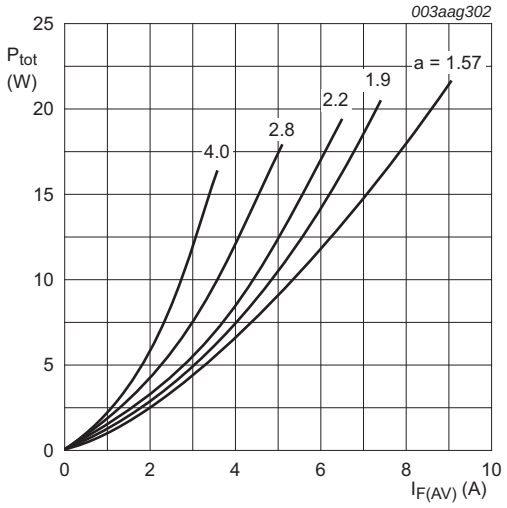
Symbol	Parameter	Conditions	Min	Max	Unit
V_{RRM}	repetitive peak reverse voltage		-	600	V
V_{RWM}	crest working reverse voltage		-	600	V
V_R	reverse voltage	DC	-	500	V
$I_{F(AV)}$	average forward current	square-wave pulse; δ = 0.5 ; T_h = 41 °C; see Figure 1; see Figure 2	-	10	A
I_{FRM}	repetitive peak forward current	square-wave pulse; δ = 0.5 ; t_p = 25 µs; T_h = 41 °C	-	20	A
I_{FSM}	non-repetitive peak forward current	t_p = 10 ms; sine-wave pulse; $T_{j(init)}$ = 25 °C; see Figure 3	-	65	A
		t_p = 8.3 ms; sine-wave pulse; $T_{j(init)}$ = 25 °C; see Figure 3	-	71	A
T_{stg}	storage temperature		-40	150	°C
T_j	junction temperature		-	150	°C



$$I_{F(AV)} = I_{F(RMS)} \times \sqrt{\delta}$$

$$V_o = 0.987\ V;\ R_s = 0.065\ \Omega$$

Fig 1. Forward power dissipation as a function of average forward current; square waveform; maximum values



$$a = \text{form factor} = I_{F(RMS)} / I_{F(AV)}$$

$$V_o = 0.987\ V;\ R_s = 0.065\ \Omega$$

Fig 2. Forward power dissipation as a function of average forward current; sinusoidal waveform; maximum values

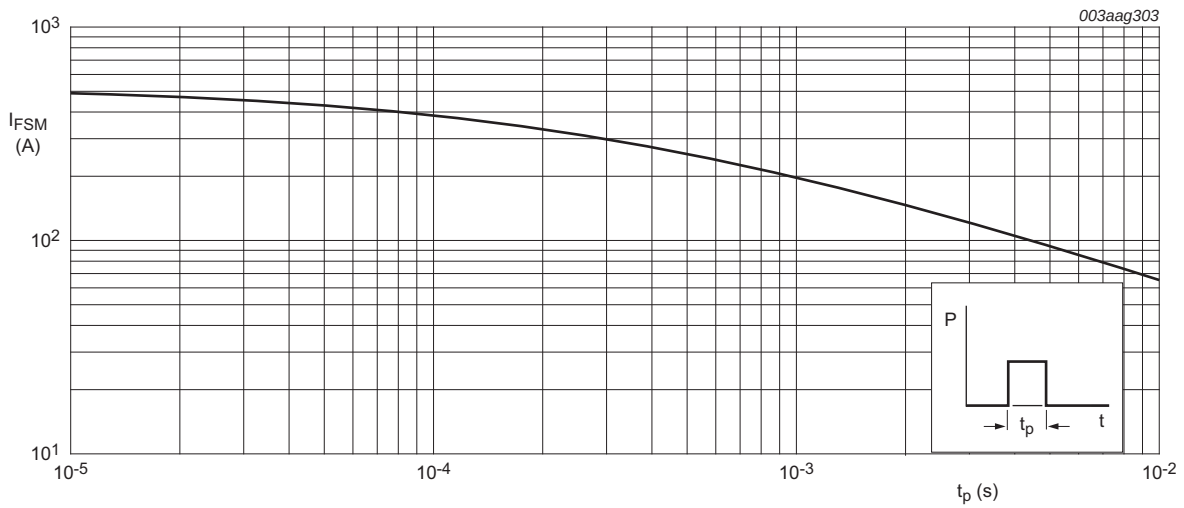


Fig 3. Non-repetitive peak forward current as a function of pulse width; square waveform; maximum values

5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-h)}$	thermal resistance from junction to heatsink	without heatsink compound	-	-	5.9	K/W
		with heatsink compound ; see Figure 4	-	-	4.8	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient free air		-	60	-	K/W

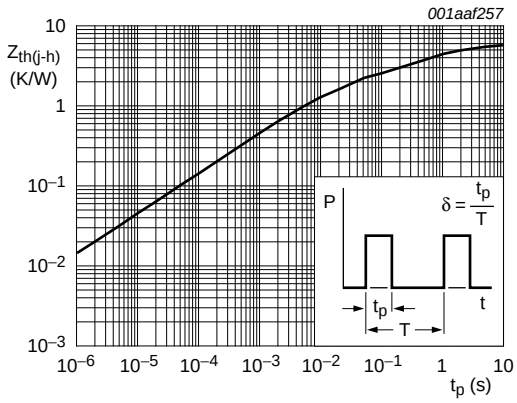


Fig 4. Transient thermal impedance from junction to heatsink as a function of pulse width

6. Isolation characteristics

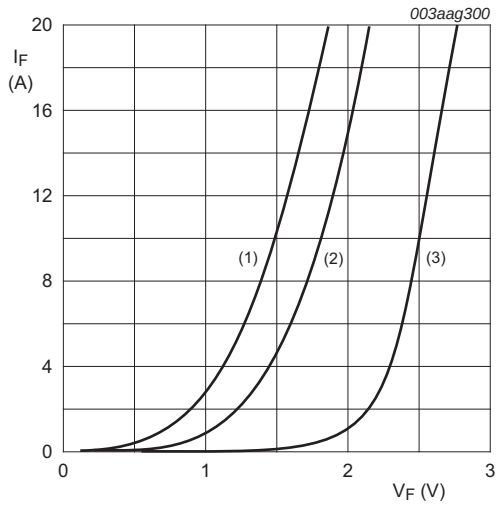
Table 6. Isolation characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{isol(RMS)}$	RMS isolation voltage	50 Hz ≤ f ≤ 60 Hz; RH ≤ 65 %; from all pins to external heatsink; sinusoidal waveform; clean and dust free	-	-	2500	V
C_{isol}	isolation capacitance	f = 1 MHz ; from cathode to external heatsink	-	10	-	pF

7. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static characteristics						
V_F	forward voltage	I_F = 10 A; T_j = 25 °C; see Figure 5	-	2	2.5	V
		I_F = 10 A; T_j = 150 °C; see Figure 5	-	1.4	1.8	V
		I_F = 20 A; T_j = 150 °C; see Figure 5	-	1.7	2.2	V
I_R	reverse current	V_R = 500 V; T_j = 100 °C	-	1.1	3	mA
		V_R = 600 V	-	9	200	µA
Dynamic characteristics						
t_{rr}	reverse recovery time	I_F = 1 A; V_R = 30 V; dI_F/dt = 50 A/µs; T_j = 25 °C; see Figure 6	-	15	30	ns
		I_F = 10 A; V_R = 400 V; dI_F/dt = 500 A/µs; T_j = 25 °C; see Figure 6	-	18	-	ns
I_{RM}	peak reverse recovery current	I_F = 10 A; V_R = 400 V; dI_F/dt = 500 A/µs; T_j = 100 °C; see Figure 6	-	9.5	12	A
		I_F = 10 A; V_R = 400 V; dI_F/dt = 50 A/µs; T_j = 125 °C; see Figure 6	-	3	7.5	A
V_{FR}	forward recovery voltage	I_F = 10 A; dI_F/dt = 100 A/µs; T_j = 25 °C; see Figure 7	-	8	11	V



(1) $T_j = 150$ °C; typical values;
(2) $T_j = 150$ °C; maxium values;
(3) $T_j = 25$ °C; maxium values;
$V_o = 0.987$ V; $R_s = 0.065\ \Omega$

Fig 5. Forward current as a function of forward voltage

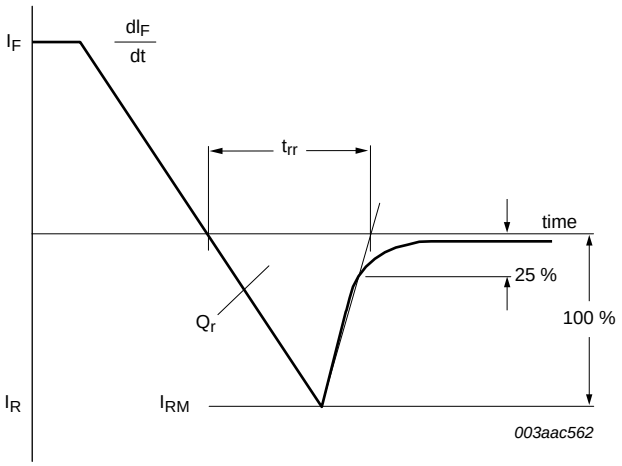


Fig 6. Reverse recovery definitions; ramp recovery

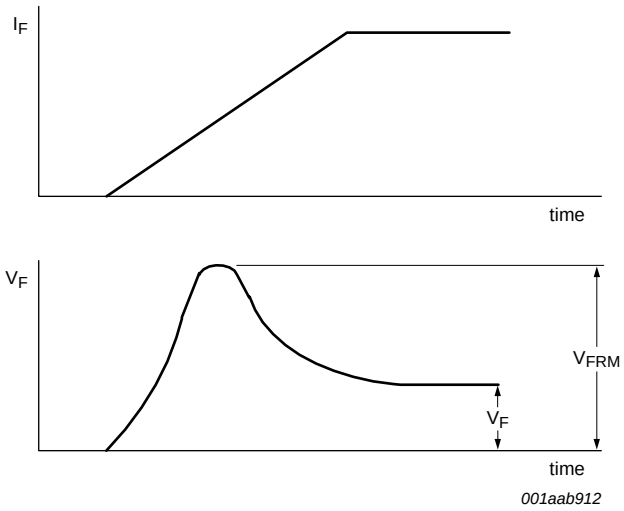
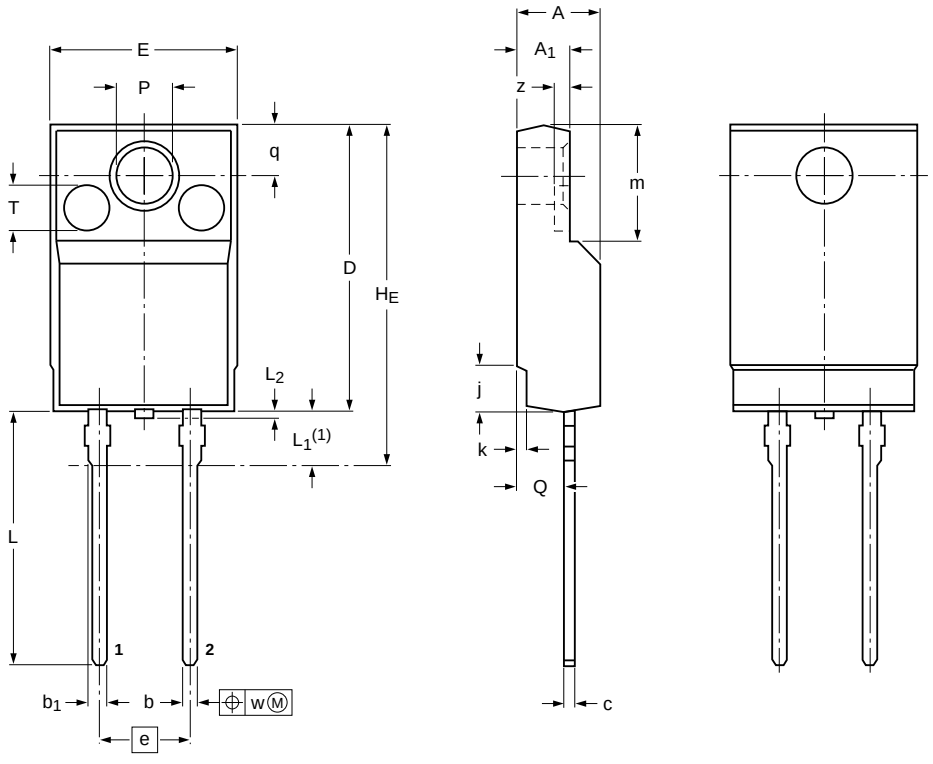


Fig 7. Forward recovery definitions

8. Package outline

Plastic single-ended package; isolated heatsink mounted;
1 mounting hole; 2-lead TO-220 'full pack' **SOD113**



z(2)
0.8

DIMENSIONS (mm are the original dimensions)

UNIT	A	A_1	b	b_1	c	D	E	e	H_E max	j	k	L	L_1(1)	L_2 max	m	P	Q	q	T	w
mm	4.6 4.0	2.9 2.5	0.9 0.7	1.1 0.9	0.7 0.4	15.8 15.2	10.3 9.7	5.08	19.0	2.7 1.7	0.6 0.4	14.4 13.5	3.3 2.8	0.5	6.5 6.3	3.2 3.0	2.6 2.3	2.6	2.55	0.4

Notes
1. Terminals are uncontrolled within zone L_1.
2. z is depth of T.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOD113		2-lead TO-220F				~~02-04-09~~ 07-06-18

Fig 8. Package outline SOD113 (TO-220F)

9. Revision history

Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BYC10DX-600 v.1	20110630	Product data sheet	-	-

10. Legal information

10.1 Data sheet status

Document status [1] [2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

10.2 Definitions

Preview — The document is a preview version only. The document is still subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

10.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nxp.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific NXP Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. NXP Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without NXP Semiconductors' warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond NXP Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies NXP Semiconductors for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond NXP Semiconductors' standard warranty and NXP Semiconductors' product specifications.

10.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Adelante, **Bitport**, **Bitsound**, **CoolFlux**, **CoReUse**, **DESFire**, **EZ-HV**, **FabKey**, **GreenChip**, **HiPerSmart**, **HITAG**, **I²C-bus** logo, **ICODE**, **I-CODE**, **ITEC**, **Labelution**, **MIFARE**, **MIFARE Plus**, **MIFARE Ultralight**, **MoReUse**, **QLPAK**, **Silicon Tuner**, **SiliconMAX**, **SmartXA**, **STARplug**, **TOPFET**, **TrenchMOS**, **TriMedia** and **UCODE** — are trademarks of NXP B.V.

HD Radio and **HD Radio** logo — are trademarks of iBiquity Digital Corporation.

11. Contact information

For more information, please visit: http://www.nxp.com

For sales office addresses, please send an email to: salesaddresses@nxp.com

12. Contents

For more information, please visit: http://www.nxp.com
For sales office addresses, please send an email to: salesaddresses@nxp.com

Date of release: 30 June 2011
Document identifier: BYC10DX-600